SIM CARD PROTOCOLS

Though rarely thought about by most users their mobile phone contains a remarkable computing device that enables them to go about their business of making calls, text messaging or playing online games. The Subscriber Identity Module (SIM) inside the mobile phone is actually a fully fledged microcomputer with an operating and file system. The protocols used between the mobile equipment or card accepting device and the SIM are a combination of protocol types that should be familiar to anyone who has studied the Open Systems Interconnection (OSI) Reference Model—that is to say the data link and application layers (though the T=0 model mixes layers).

This paper attempts in broad strokes to outline the construction of these protocols and how they are used.

Card Power-Up

When a smart card, or in our discussion SIM card, is inserted into a card reader or mobile handset the card is not immediately powered up. Doing so without out at first testing if the card is properly inserted can destroy the card.

Once the proper insertion of the card has been detected, the card accepting device (CAD) or mobile equipment (ME) the CAD or ME first powers the card to a normal operating voltage and the card is brought to an idle state. Then a RST command is sent over the contact line (C2) on the face of the SIM card. The RST command remains in a low state for a set period of time and then is set to a high state. This is a signal to the SIM card to begin its initialization sequence.

The initialization sequence ends with the SIM card sending an Answer to Reset (ATR). Its primary purpose is to indicate the status of the smart card power-up sequence. It also conveys information which the reader requires in order to optimize the speed of communication between the reader and the card. The total length of the ATR sequence is limited to 33 bytes (The ATR must adhere to the structure specified by ISO 7816-3). The table shown below illustrates the ATR as outlined in ISO 7816-3.

<table>
<thead>
<tr>
<th>Character ID</th>
<th>Definition</th>
</tr>
</thead>
<tbody>
<tr>
<td><strong>Initial Character Section</strong></td>
<td></td>
</tr>
<tr>
<td>TS</td>
<td>Mandatory initial character</td>
</tr>
<tr>
<td><strong>Format Character Section</strong></td>
<td></td>
</tr>
<tr>
<td>T0</td>
<td>Indicator for presence of interface characters</td>
</tr>
<tr>
<td><strong>Interface Character Section</strong></td>
<td></td>
</tr>
<tr>
<td>TA1</td>
<td>Global, codes F1 and D1</td>
</tr>
<tr>
<td>TB1</td>
<td>Global, codes 11 and PI1</td>
</tr>
<tr>
<td>TC1</td>
<td>Global, code N</td>
</tr>
</tbody>
</table>
TD₁ Codes Y₂ and T
TA₂ Specific
TB₂ Global, code Pi2
TC₂ Specific
TD₂ Codes Y₃ and T
TA₃ TAᵢ, TBᵢ and TCᵢ are specific
...TDᵢ Codes Yᵢ₊₁ and T

Historical Character Section
T₁ Card specific information
...TK (Maximum of 15 characters)

Check Character Section
TCK₂ Optional check character

Table 1: ATR structure¹.

TS and T0 are the only mandatory bytes in the ATR sequence. The initial character TS is used to establish bit-signaling and bit-ordering conventions. T0 is used to indicate the presence or absence of subsequent interface or historical characters. The upper 4 bits or nibble (bits 5 - 8) is designated Y1 and signals the presence of optional characters based on a logic 1 in the following bit positions:

- Bit 5 indicates TA₁ is present
- Bit 6 indicates TB₁ is present
- Bit 7 indicates TC₁ is present
- Bit 8 indicates TD₁ is present

The lower nibble (bits 1 - 4) is designated K and is interpreted as a numeric value in the range 0 - 15. It indicates the number of historical characters present.

The interface characters are used to select the protocol and parameters used for subsequent higher-level communication between the smart card and the reader. The TA₁ interface character is the most significant to this paper as it provides the necessary information to achieve optimal communication speeds between the reader and the card. The value of Y1 indicates whether TA₁, TB₁, TC₁ or TD₁ is present and the value of Yi (encoded in TDᵢ₋₁) determines the presence of TAᵢ, TBᵢ, TCᵢ and TDᵢ. The purpose of a few of the most relevant of these is explained below.

- TA₁: Encodes FI in the upper nibble (bits 5 - 8) and DI the lower nibble (bits 1 - 4). These are used to determine the clock conversion factor (F) and the bit rate adjustment factor (D) and maximum supported clock rate according to the tables below. The default values of F and D if unspecified are: F = 372 and D = 1.
<table>
<thead>
<tr>
<th>FI</th>
<th>0000</th>
<th>0001</th>
<th>0010</th>
<th>0011</th>
<th>0100</th>
<th>0101</th>
<th>0110</th>
<th>0111</th>
</tr>
</thead>
<tbody>
<tr>
<td>F</td>
<td>Internal clock</td>
<td>372</td>
<td>558</td>
<td>744</td>
<td>1116</td>
<td>1488</td>
<td>1860</td>
<td>RFU</td>
</tr>
<tr>
<td>$f_{\text{max}}$ (MHz)</td>
<td>-</td>
<td>5</td>
<td>6</td>
<td>8</td>
<td>12</td>
<td>16</td>
<td>20</td>
<td>-</td>
</tr>
</tbody>
</table>

Table 2: Values of the upper nibble of the TA1 byte of the ATR$^1$.

<table>
<thead>
<tr>
<th>FI</th>
<th>1000</th>
<th>1001</th>
<th>1010</th>
<th>1011</th>
<th>1100</th>
<th>1101</th>
<th>1110</th>
<th>1111</th>
</tr>
</thead>
<tbody>
<tr>
<td>F</td>
<td>RFU</td>
<td>512</td>
<td>768</td>
<td>1024</td>
<td>1536</td>
<td>2048</td>
<td>RFU</td>
<td>RFU</td>
</tr>
<tr>
<td>$f_{\text{max}}$ (MHz)</td>
<td>-</td>
<td>5</td>
<td>7.5</td>
<td>10</td>
<td>15</td>
<td>20</td>
<td>-</td>
<td>-</td>
</tr>
</tbody>
</table>

Table 3: Values of the lower nibble of the TA1 byte of the ATR$^1$.

- TB$_1$, TB$_2$: Used to encode information concerning the programming voltage and current.  
- TC$_1$: Interpreted as an 8-bit unsigned integer representing the extra guard time required between characters (N). The default value of N is 0.  
- TD$_1$: Encodes Y1 in upper nibble and the protocol type in the lower nibble. ISO 7816-3 defines two protocols: the T=0 protocol and the T=1 protocol. T=0 is an asynchronous character-oriented protocol where an acknowledgement must be received for every byte that is sent. In contrast, T=1 is an asynchronous block-oriented protocol where a number of bytes can be sent before an acknowledgement must be received. If TD$_1$ is not present, then T=0 is used implicitly.
The historical characters are usually used to indicate the type, model and use of the specific card. These are generally defined by the manufacturer or card issuer. There is no established standard for the data in these historical bits. The check character (TCK) is used to determine whether a transmission error occurred in sending the ATR from the card to the reader. TCK is a checksum calculated such that performing a bit-wise exclusive-or (XOR) operation on all bytes in the ATR from T0 to TCK results in an answer of zero.

**Transmission Protocol Data Units**

Transmission Protocol Data Units (TPDUs) are the data structures passed back and forth by the SIM and the CAD or ME. The two protocols commonly used for this communication are the T=0 protocol and the T=1 protocol.

**T=0**

This protocol is a byte oriented protocol, meaning that the minimum unit of information transferred across the I/O channel is a byte. Error handling for this protocol is the same. This protocol tends to mix elements from the link-level and application-level protocol layers.

The TPDU of the T=0 protocol is comprised of two data structures—one sent from a reader or device to the card and one sent back from the card(similar to command and response). The command header of the T=0 protocol includes the following five fields.

- **CLA**—this is a one byte field that is a collection of instructions
- **INS**—This is a one byte field that specifies a specific instruction to the card from within the set of instructions in the CLA
- **P1**—This is a one byte field that used to specify the addressing used by the INS and CLA instruction
- **P2**—this is a one byte field also used in addressing
- **P3**—this is a one byte field that is used to specify the number of bytes transferred to or from the card as part of the instruction execution.

The below table lists some of the instructions that can be contained in the CLA byte.
<table>
<thead>
<tr>
<th>CLA Byte</th>
<th>Instruction Set</th>
</tr>
</thead>
<tbody>
<tr>
<td>0X</td>
<td>ISO 7816-4 instructions (files and security)</td>
</tr>
<tr>
<td>10 to 7F</td>
<td>Reserved for future use</td>
</tr>
<tr>
<td>8X or 9X</td>
<td>ISO 7816-4 instructions</td>
</tr>
<tr>
<td>AX</td>
<td>Application/vendor specific instructions</td>
</tr>
<tr>
<td>B0 to CF</td>
<td>ISO 7816-4 instructions</td>
</tr>
<tr>
<td>D0 to FE</td>
<td>Application/vendor specific instructions</td>
</tr>
<tr>
<td>FF</td>
<td>Reserved for protocol type selection</td>
</tr>
</tbody>
</table>

Table 4: CLA instruction set definitions.

The next table shows the instructions set in the INS byte for security and file system access on the card.

<table>
<thead>
<tr>
<th>INS Value</th>
<th>Command Name</th>
<th>INS Value</th>
<th>Command Name</th>
</tr>
</thead>
<tbody>
<tr>
<td>0E</td>
<td>Erase Binary</td>
<td>C0</td>
<td>Get Response</td>
</tr>
<tr>
<td>20</td>
<td>Verify</td>
<td>C2</td>
<td>Envelope</td>
</tr>
<tr>
<td>70</td>
<td>Manage Channel</td>
<td>CA</td>
<td>Get Data</td>
</tr>
<tr>
<td>82</td>
<td>External Authenticate</td>
<td>D0</td>
<td>Write Binary</td>
</tr>
<tr>
<td>84</td>
<td>Get Challenge</td>
<td>D2</td>
<td>Write Record</td>
</tr>
<tr>
<td>88</td>
<td>Internal Authenticate</td>
<td>D6</td>
<td>Update Binary</td>
</tr>
<tr>
<td>A4</td>
<td>Select File</td>
<td>DA</td>
<td>Put Data</td>
</tr>
<tr>
<td>B0</td>
<td>Read Binary</td>
<td>DC</td>
<td>Update Record</td>
</tr>
<tr>
<td>B2</td>
<td>Read Record(s)</td>
<td>E2</td>
<td>Append Record</td>
</tr>
</tbody>
</table>

Table 5: ISO 7816-4 INS codes.

As was mentioned before the T=0 protocol tends to mix elements from other levels in the OSI model. The P1 and P2 parameters, although defined at the link level protocol level are in truth dependant on the instruction that is specified. In other words they are dependant on the application protocol information. These two parameters provide control or addressing for application-specific instructions such as the Select File instruction which involves the selection of a specific file in the file system which then allows for other operations such as writing or reading.
P3 is also an application-level parameter. P3 generally specifies the number of bytes to be transmitted during the INS specified instruction execution. Data is either outgoing-moving from card to reader or incoming-from reader to card.

After each command TPDU, a response TPDU is returned. This TPDU is made up of a number of procedure bytes. This TPDU contains three Mandatory fields and one optional one.

- **ACK**: indicates that the card has received the [CLA, INS] command
- **NULL**: used for flow control on the I/O channel by the card. It signals (to the reader) that the card is still processing the command and so the reader must wait before sending another command
- **SW1**: status response of the current command
- **SW2**: (optional) also conveys a status response to the reader

The ACK byte is a repeat of the INS byte that was sent by the command TPDU. The second byte NULL, is a way to mark the time period for processing the command. If a response is not received within a timeout period the reader of device may send out a RST sequence to reinitialize the protocol. A least one NULL response received by the reader will prevent this from occurring.

The SW1 is a status byte from the card to the reader that will tell the result of the sent instruction; in certain instructions the card may return data bytes to the reader or equipment. If this is the case the SW2 status byte is returned. This is a trigger for the reader to now execute another command-the **GetResponse** command, which actually returns the data from the previous executed instruction.

Error checking occurs in the T=0 protocol with a parity check. For every byte transferred 11 bits must be used. The parity bit is cleared or set to make the total number of bits set be even. The receiving side of the channel can look at the bits transferred prior to the parity bit and determine what parity value to expect. If the actual transmission does not match what was expected it can be assumed that an error has occurred and a recovery procedure must take place. This recovery is initialized on the receiving side and involves the re-transmission of the byte received in error.

**T=1**

This protocol is block-oriented; meaning that a defined collection of data or block is moved as a whole between the card and reader. This block of data may contain an Application Protocol Data Unit (APDU) defined for a specific application. This shows a good example of the layering between the link and application protocol layers. Moving the information as a single block calls for error free transmission and this error detection and correction is much more complex for the T=1 protocol than in the T=0 protocol.
This error detection is done using a longitudinal redundancy character (LRC)-a slightly more complex parity type check that exists in the T=0 protocol or using a cyclical redundancy check (CRC). The specific CRC algorithm used is defined in ISO 3309.

The T=1 protocol uses three types of blocks:

1. Information block: Used to exchange data between application software on the card and application software on the reader side of the channel.
2. Receive ready block: Used for positive and negative acknowledgements on either end of the channel. A positive acknowledgement indicates that the block was correctly received. Conversely a negative acknowledgement indicates that an error was detected in the block received.
3. Supervisory block: Used to transfer control information between the reader and the card.

Each of the data blocks has the same structure comprising of the following fields.

- Prologue field: Mandatory field with a size of three bytes including the following three elements:
  - NAD: Node Address is used to identify the addresses of the source and the intended destination of the block.
  - PCB: Protocol Control Byte is used to indicate the type of block (either information, receive ready or supervisory)
  - LEN: Length of the block
- Information field: Optional field that may be up to 254 bytes in length and may contain an APDU
- Epilogue field: Mandatory field that is either 1 or 2 bytes in length and is used for error detection

When used the NAD element also contains two subfields: SAD-which is the source address shown by the three low order bits of the NAD and DAD, which is the destination address which is shown in bits five through seven of the NAD.

In the PCB, the two most significant bits (high order) of the byte indicate of which type the block is:

- A high order set to zero indicates and informational block
- The two high order bits set to one indicate a supervisory block
- The high order bit set to one and the other to zero indicates a receive ready block

**Application Protocol Data Units**

The ISO 7816-4 standard addresses two areas of functionality for application software:
- File system: A set of functions is provided in the form of an API. By using this API application software on the reader side can access files in the file system.
- Security functions: These can be used to limit access to application software or to files on the card.

The T=0 or T=1 protocols are used to support application-level protocols between the smart card application and the reader application. These application protocols exchange data structures called application protocol data units (APDUs). The following diagram illustrates this architecture:

![Application communication architecture diagram](image)

**Figure 1**: Application communication architecture.

The APDU structure defined by ISO 7816-4 is very similar to the TPDU structure used in the T=0 protocol. In fact, when an APDU is transported by the T=0 protocol, the elements of the APDU directly overlay the elements of the TPDU.

The APDU as defined by ISO 7816 is link-level protocol independent and also defined at the application level.

**APDU Structure**

There are two types of messages used to support the ISO 7816-4 application protocols: the *command APDU* (sent from the reader to the card) and the *response APDU* (sent from the card to the reader).
The command APDU consists of a header and a body (shown above). The header includes CLA, INS, P1 and P2 fields. As in the T=0 protocol, CLA and INS specify an application class and instruction. P1 and P2 are used to qualify specific instructions and are given specific definitions by each [CLA, INS] instruction. The body of the APDU can vary in size and is used to transmit data to the card’s APDU processor as part of a command or to convey a response from the card to the reader. The Lc field specifies the number of bytes to be transmitted to the card as part of the instruction-length of the data field. The data field contains information that must be sent to the card to allow its APDU processor to execute the command specified in the APDU. The Le field specifies the number of bytes that will be returned to the reader in the response APDU.

The body of the APDU can appear in four forms:

1. No data is transferred to or from the card, so the APDU only contains the header.
2. No data is transferred to the card, but data is returned from the card. The body of the APDU only contains a non-null Le field.
3. Data is transferred to the card, but none is returned from it. The body of the APDU includes the Lc and data fields.
4. Data is transferred to the card and is also returned from the card as a result of the command. The body of the APDU includes the Lc, data and Le fields.

The response APDU consists of a body and a trailer- considerably simpler than that of a command APDU. The body is either null or it includes a data field – this depends on the specific command and its success or failure by the APDU processor. If the APDU contains a data field its length is determined by the Le field in the corresponding command APDU.
The trailer consists of up to two fields of status information called SW1 and SW2. These fields return a status code in which one byte is used to specify an error category and the other is used to specify a command-specific status or error indication.

The error codes follow the ISO 7816 numbering scheme in which one byte is used to determine error category and the other shows the specific error.

<table>
<thead>
<tr>
<th>RETURN CODE</th>
<th>PROCESS COMPLETED</th>
<th>PROCESS INTERRUPTED</th>
</tr>
</thead>
<tbody>
<tr>
<td>Normal</td>
<td>Warning</td>
<td>Execution Error</td>
</tr>
<tr>
<td>61XX or 9000</td>
<td>62XX</td>
<td>64XX</td>
</tr>
<tr>
<td></td>
<td>63XX</td>
<td>65XX</td>
</tr>
<tr>
<td></td>
<td></td>
<td>67XX to 6FXX</td>
</tr>
</tbody>
</table>

Table 6: ISO 7816 Error Return Codes

Lastly, in the ADPU the CLA byte has two additional noteworthy features:

- The two low order bits can indicate a logical communication channel between the card APDU processor and the reader application.
- The next two high order bits can indicate that secure messaging is being used between the reader application and the card APDU.

The goal of this paper was to outline the general communication protocols that the mobile phone SIM card—which inherits these from smart cards—uses to communicate with card reader or the mobile equipment. The TPDU protocols T=0 and T=1 were discussed as well as the APDU structure. The examiner is encouraged to examine the cited works further for more in depth information regarding SIM card protocols and commands.

**CITATIONS**